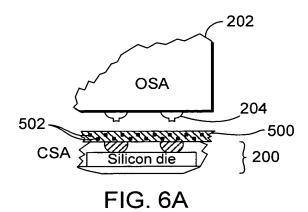
<u>REPLACEMENT SHEET</u> Techniques For Joining An Opto-Electronic Module To A Semiconductor Package

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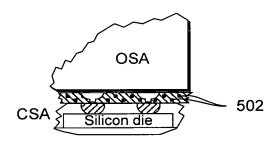
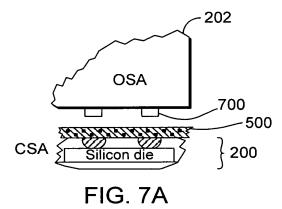


FIG. 6B



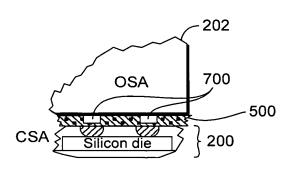


FIG. 7B

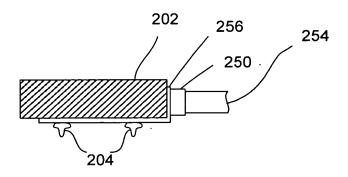


FIG. 12A

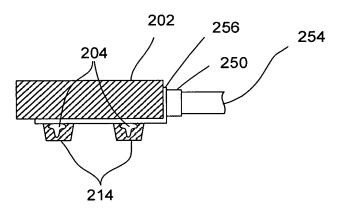


FIG. 12B

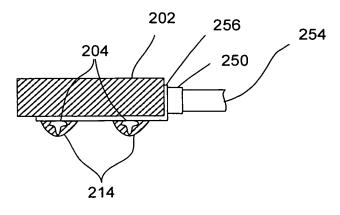


FIG. 12C

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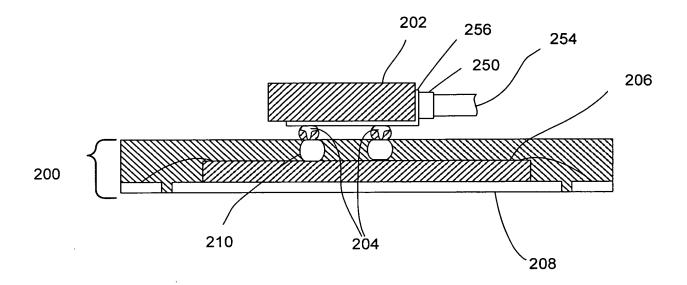


FIG. 12D

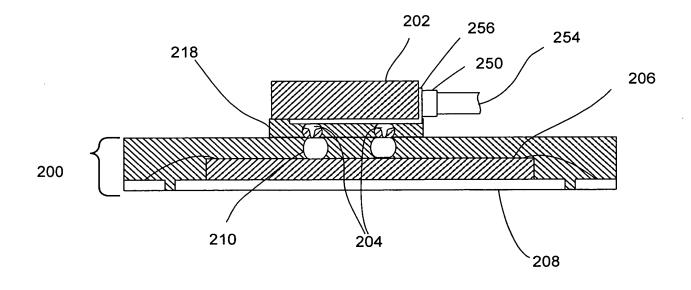


FIG. 12E